IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Teruaki Kasai

Serial No.:

N/A

Filing Date:

Herewith

Title:

"APPARATUS AND METHOD FOR PICKING UP

SEMICONDUCTOR CHIP"

Docket No.:

36635

INFORMATION DISCLOSURE STATEMENT

Mail Stop PATENT APPLICATION Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with Rule 56, applicant is aware of the publications listed in the enclosed copy of Patent Office Form 1449. A copy of each of the publications is enclosed herewith.

Respectfully submitted, PEARNE & GORDON LLP

By:

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April 8, 2004

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GROUP ART UNIT: Herewith U.S. PATENT DOCUMENTS Filing Date If Appropriate Examiner Document No. Date Name Class Subclass Initial Α 2004/0038498 2/2004 Ozono et al. В \mathbf{C} D E F G Η Ι J K FOREIGN PATENT DOCUMENTS Document No. Class Subclass Translation Date Country L cited pg 1 of 54-58356 5/1979 Japan spec 05-335405 English M 12/1993 Japan abstract N 06-061347 3/1994 **English** Japan abstract P 11-274181 10/1999 Japan English abstract Q OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.) R Examiner: **Date Considered**

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